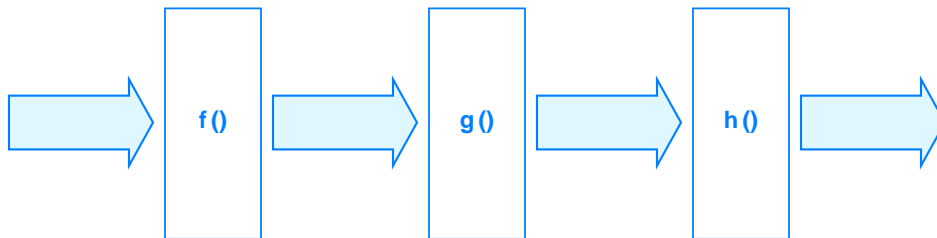


Figure 13.4 Illustration of packet flow through a parallel architecture. If the architecture has N processors, each processor handles approximately $1/N$ of the traffic.

In a pipeline architecture, each processor performs one operation. Figure 13.5 illustrates the packet flow.



Each processor performs one operation as packets flow through the pipeline."