STOCHFUZZ: Sound and Cost-effective Fuzzing of Stripped Binaries by Incremental and Stochastic Rewriting

Zhuo Zhang, Wei You, Guanhong Tao, Yousra Aafer, Xuwei Liu, Xiangyu Zhang







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Bug Detection

- More than **21,000** bugs in the Chromium projects [1]
- More than **16,000** bugs in other open source projects [2]



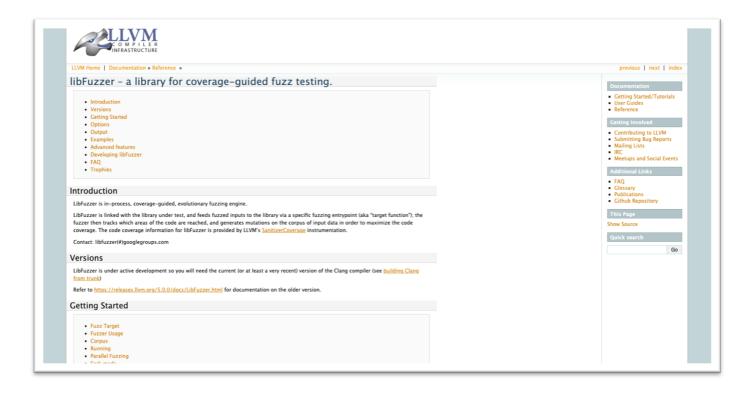
- 79 Papers published in the top security conferences in the recent three years [3]
- 56 Papers published in the top software engineering conferences in the recent three years [3]

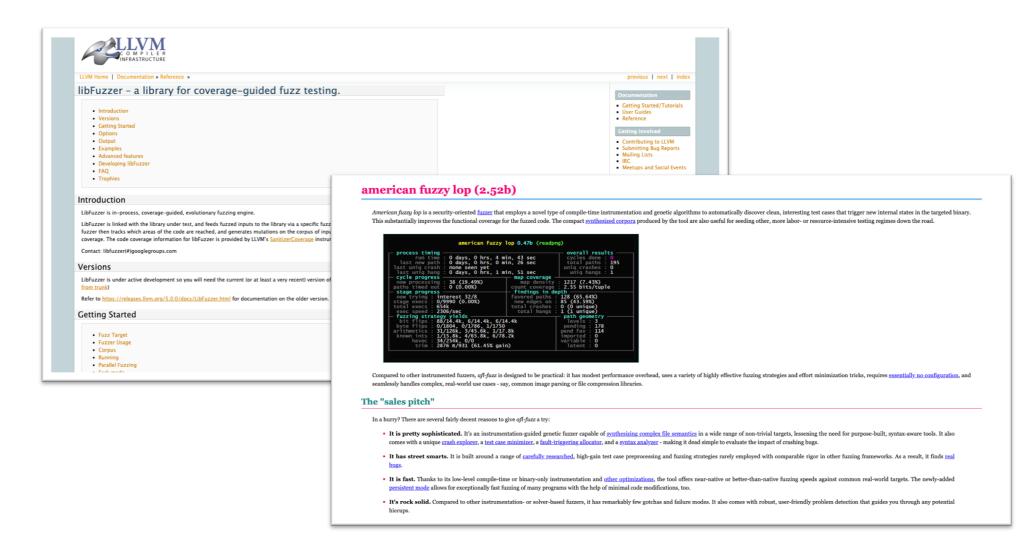
^[1] https://bugs.chromium.org/p/chromium/issues/list?can=1&q=label%3AClusterFuzz+-status%3AWontFix%2CDuplicate&colspec=ID+Pri+M+Stars+ReleaseBlock+Component+Status+Owner+Summary+OS+Modified&x=m&y=releaseblock&cells=ids

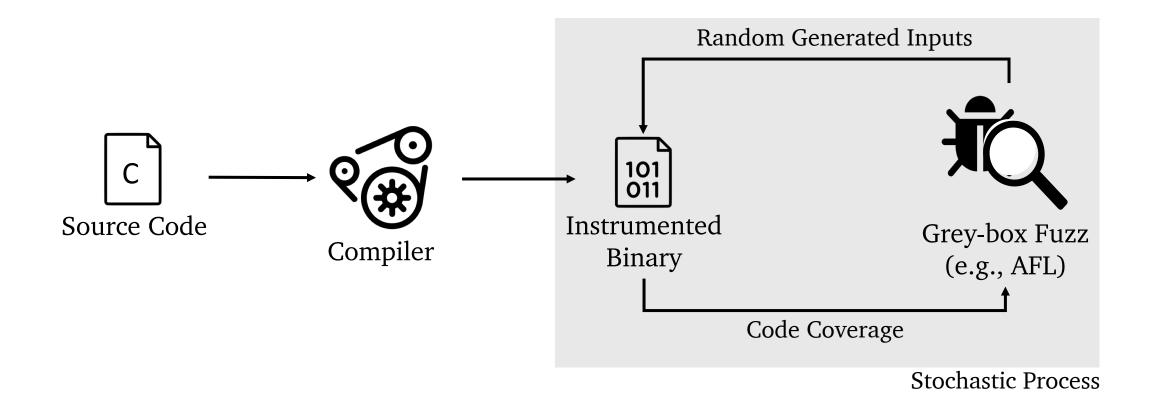
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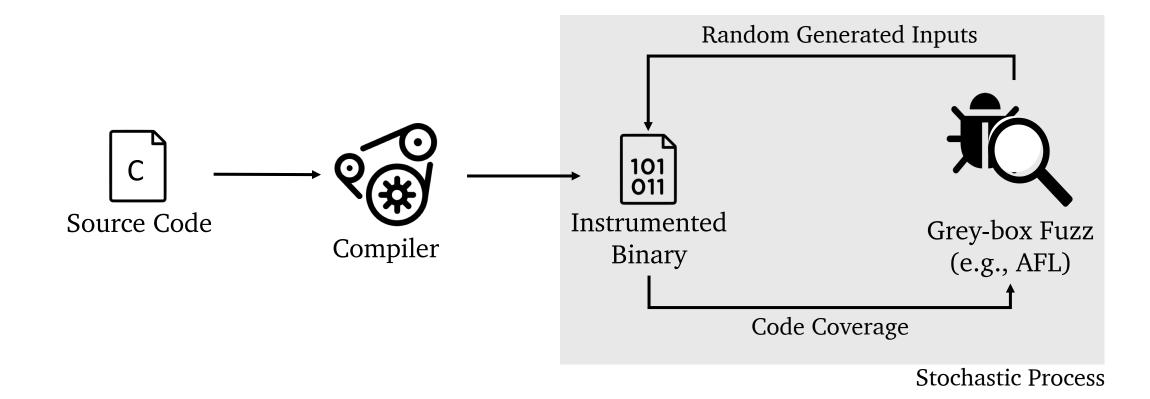
SŢOCHFUZZ: Sound and Cost-effective Fuzzing of Stripped Binaries by Incremental and Stochastic Rewriting



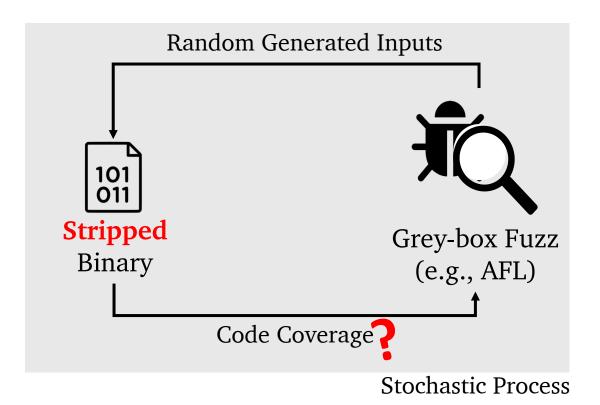




Another scenario: binary-only fuzzing (no source code)



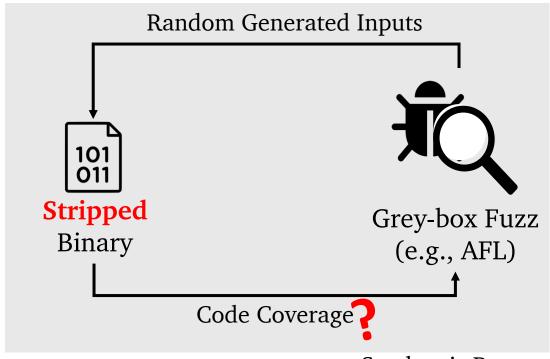
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- Bugs in close-sourced programs can also have unprecedented impact (e.g., WannaCry ransomware attack).
- It is important to effectively detect bugs in programs without source.





Stochastic Process

STOCHFUZZ: Sound and Cost-effective Fuzzing of Stripped Binaries by Incremental and Stochastic Rewriting

Existing solutions fall into three categories.



<u>Dynamic Binary Translation</u>: Translate a subject binary during its execution. It is sound but expensive (high overhead >600%).



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Static Binary Instrumentation: Leverage advanced binary analysis to directly instrument binaries (cost-effective but usually unsound).



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.DATA:
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.CODE2:

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- <u>Identify the interleaved data section</u>: due to the inline data (.DATA), rewriters may not only mis-rewrite data as code, but also fail to identify the indirect jump target (.CODE2).
- <u>Distinguish between scalars and the address offsets</u>: misclassifying an address offset (<u>.CODE2-.DATA</u>) as a scalar may break the rewritten binaries (note that addresses have changed after instrumentation).

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     or [rax], al
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RetroWrite, e9patch, and datalog disassembly (the version before we reported the issue) all fail on a similar case.

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How we handle the motivation case: Incremental Rewriting

The first technique we introduced is named Incremental Rewriting.

- While grey-box fuzzers continuously mutate inputs across test runs, they may as well be enhanced to mutate the program on-the-fly.
- As such, disassembly and static rewriting (which are difficult due to the lack of symbol information and difficulties in resolving indirect jumps/calls offline) can be *incrementally performed over time*.

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Our basic idea is to trigger an intentional crash once an unresolved control flow target is reached. Starting from the address where the crash happens, we can incrementally rewrite all directly reachable addresses.

The fuzzer continues fuzzing with the new binary and the incremental rewriting is invoked again if other intentional crashes occur.

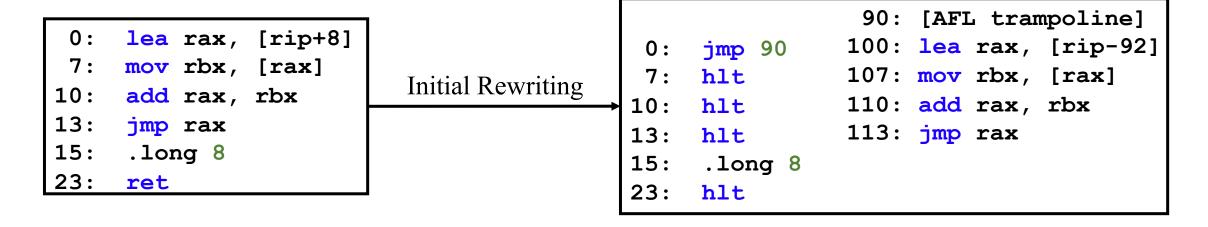
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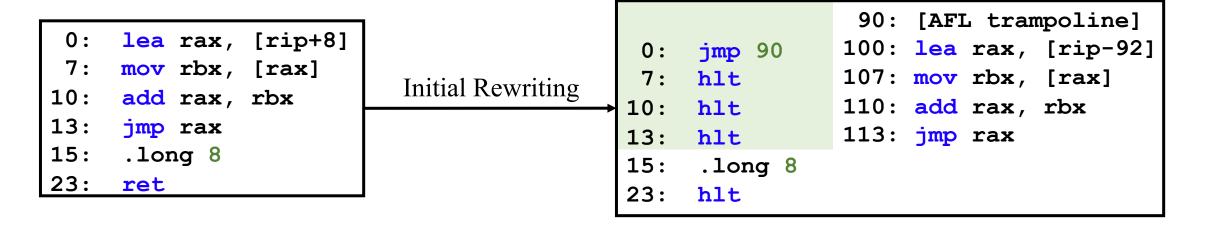
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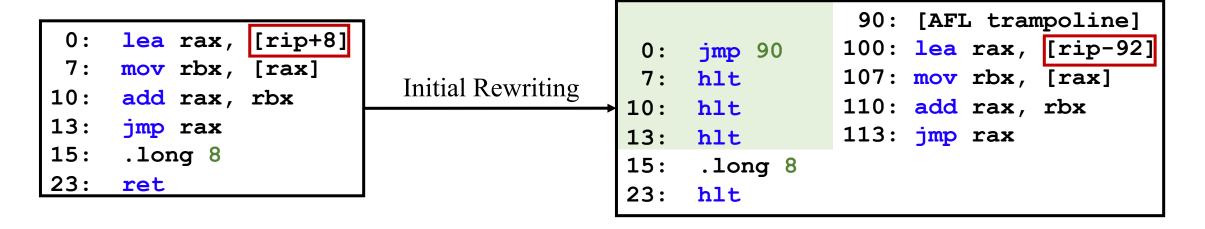
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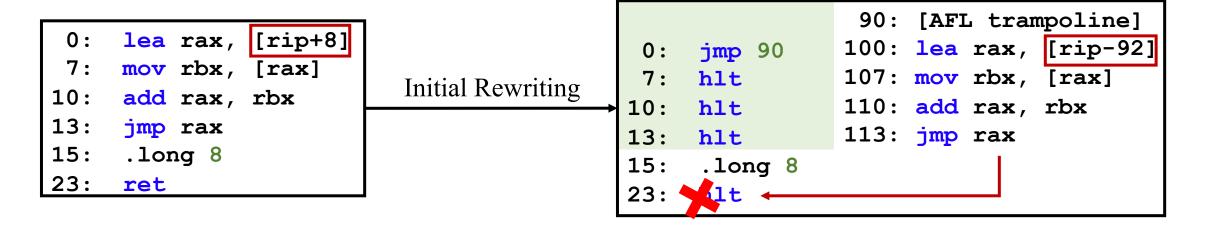


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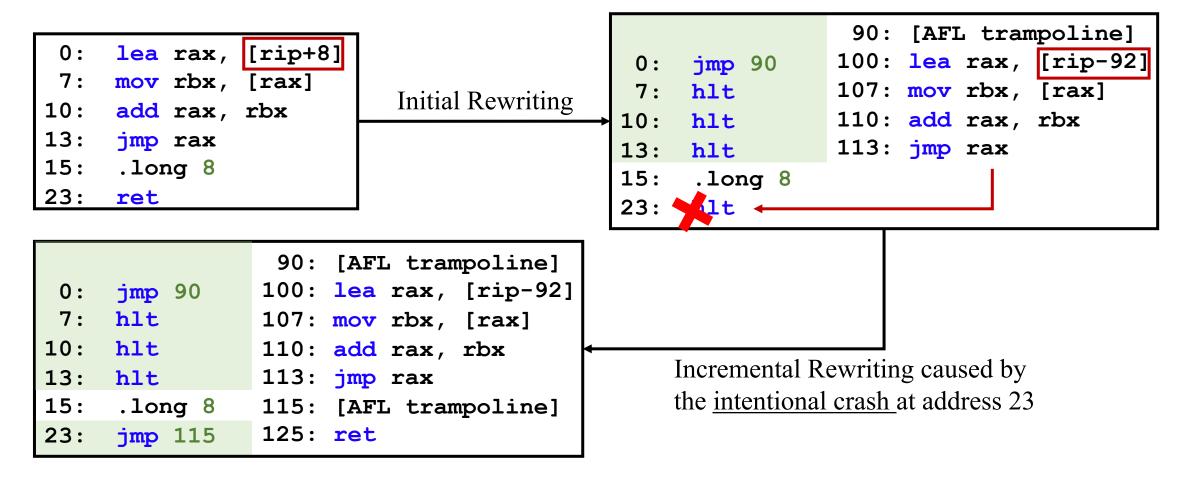
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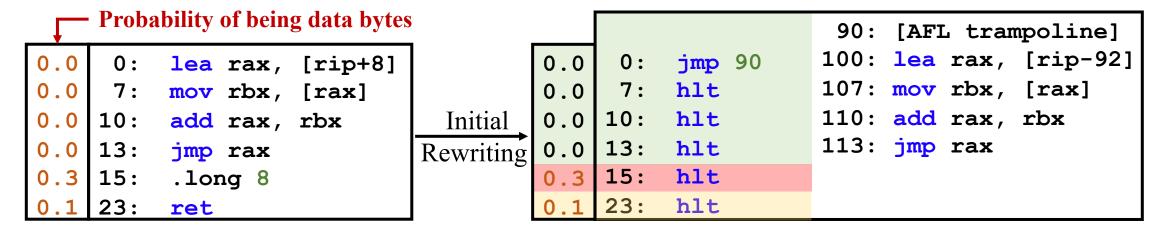
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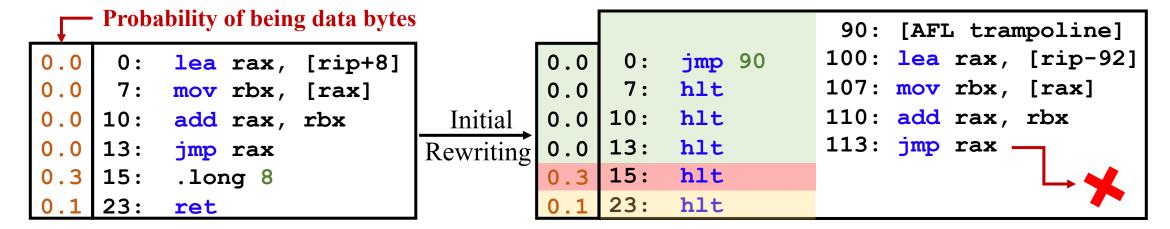
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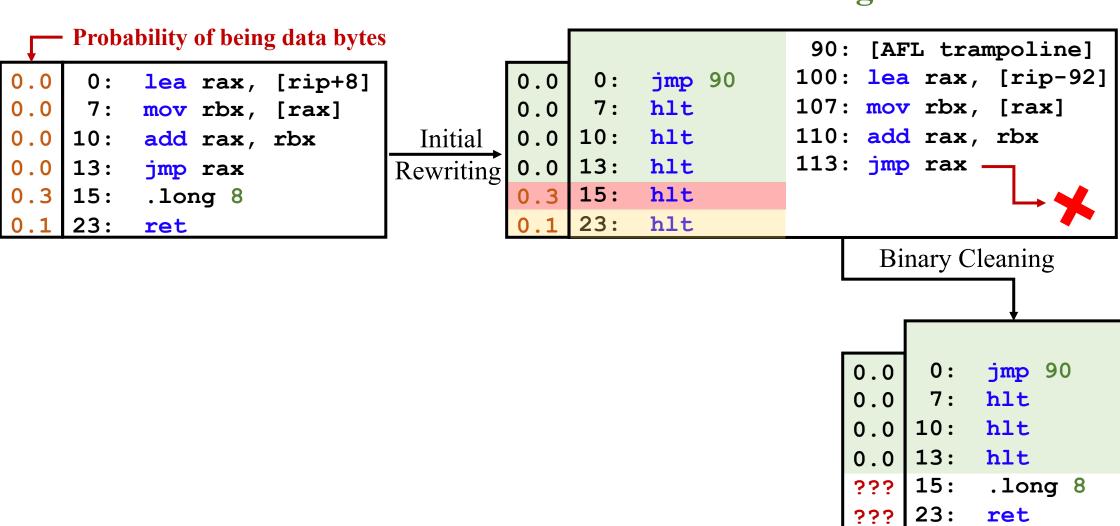
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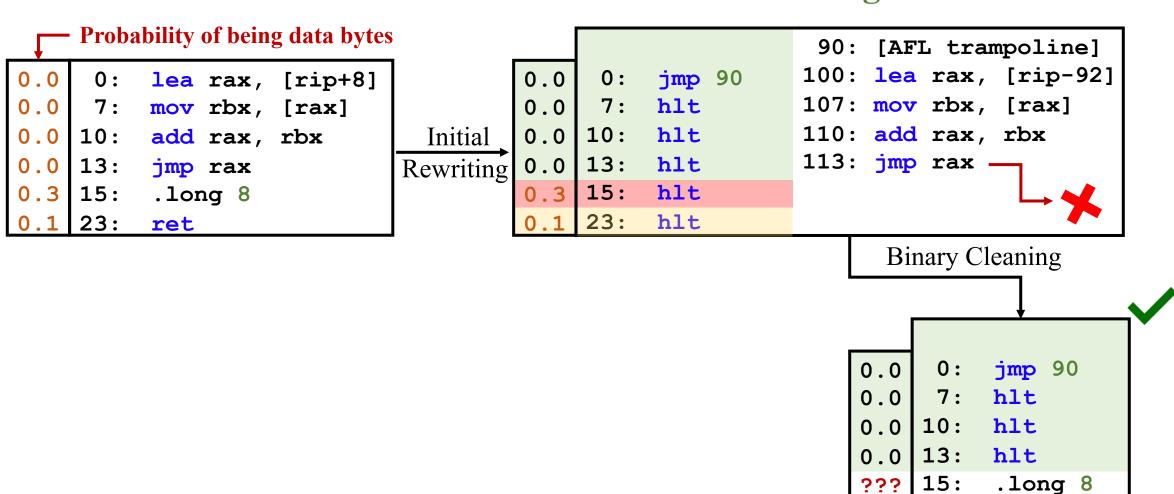
- Probability of being data bytes

```
0.0
     0:
          lea rax, [rip+8]
         mov rbx, [rax]
0.0
     7:
         add rax, rbx
    10:
0.0
   13:
          jmp rax
0.0
0.3 | 15:
          .long 8
0.1 | 23:
          ret
```



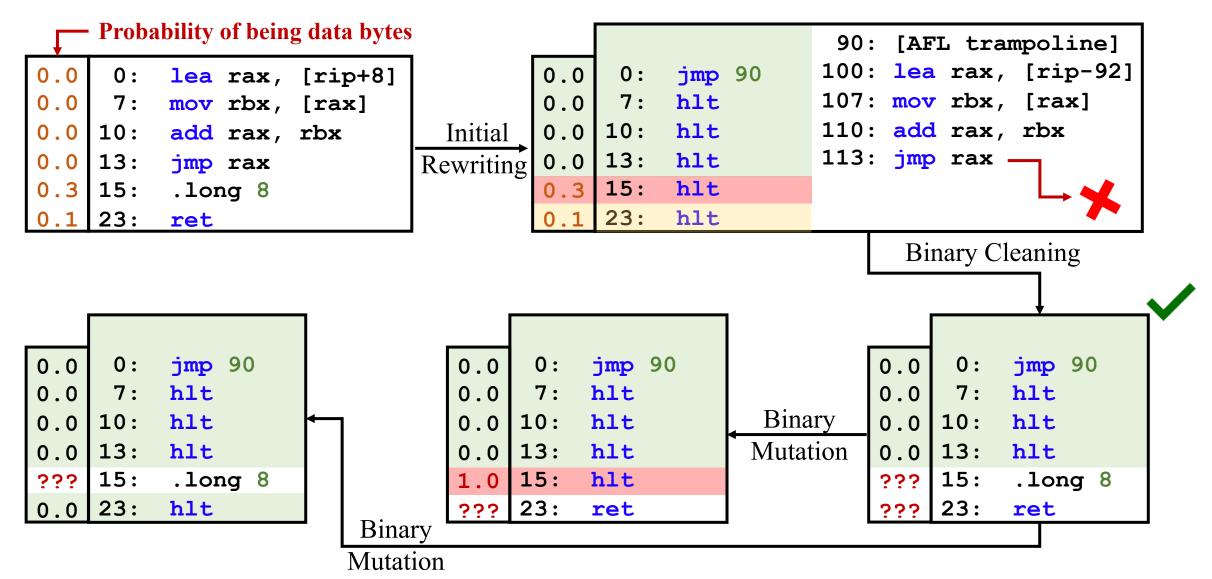


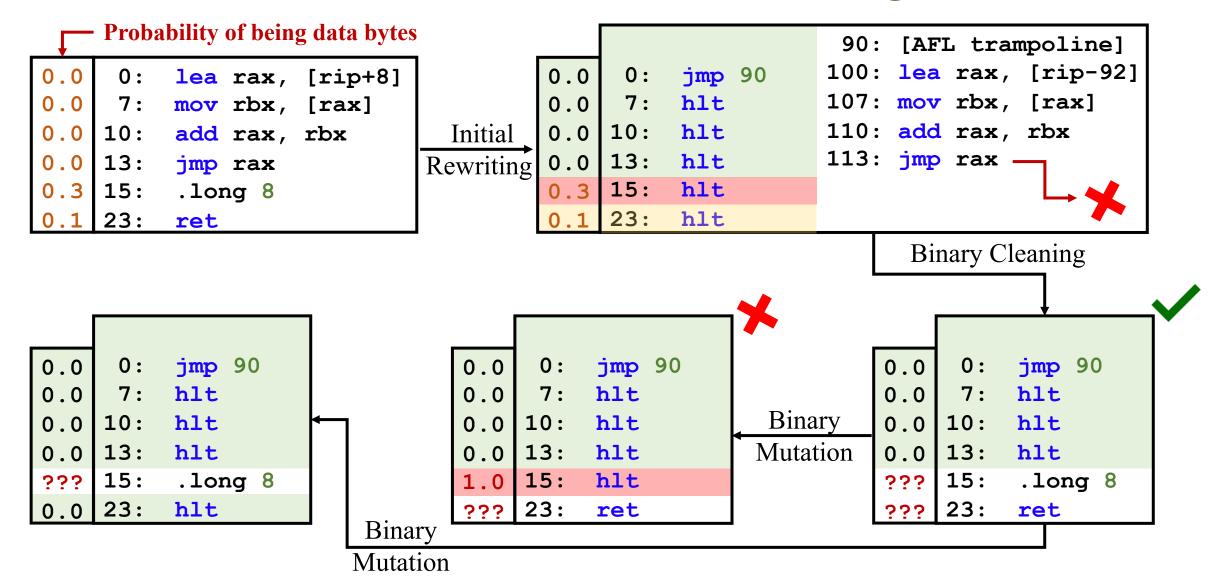


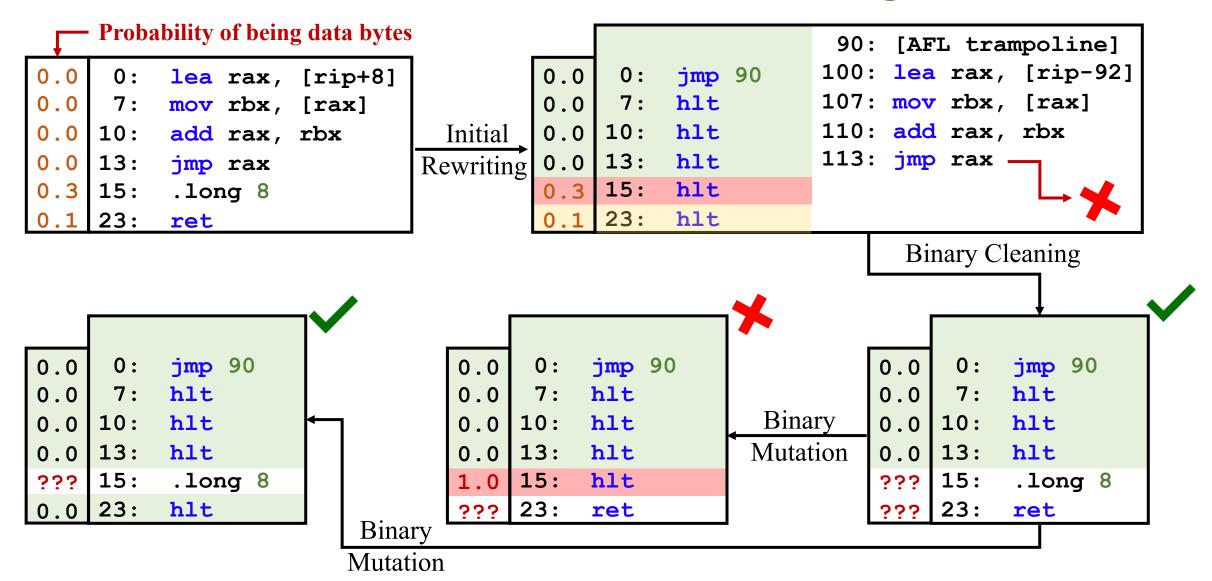


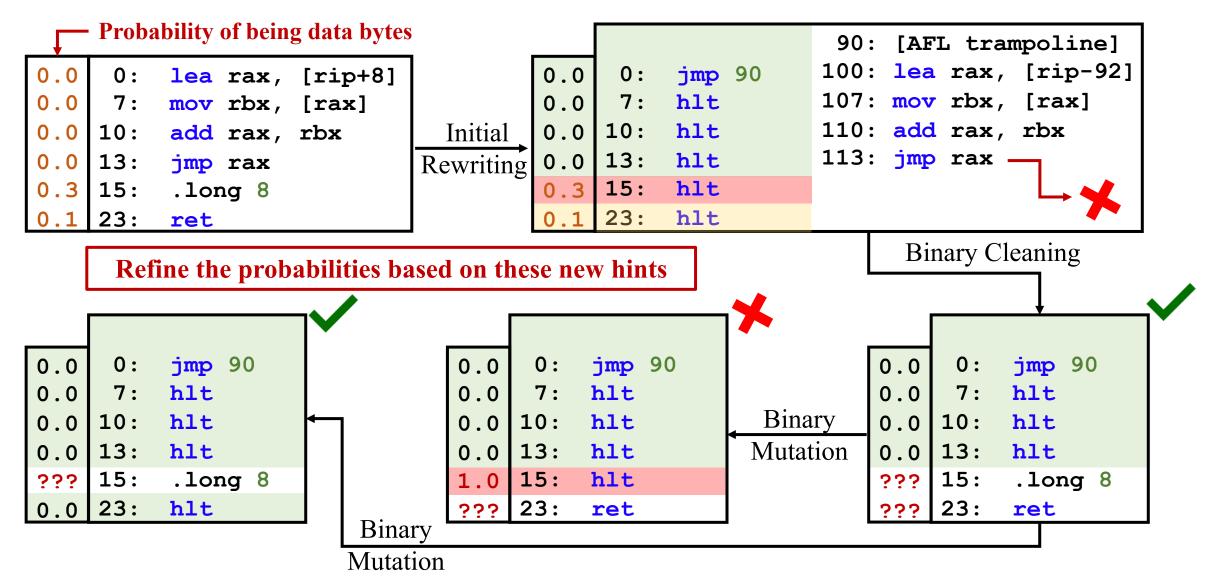
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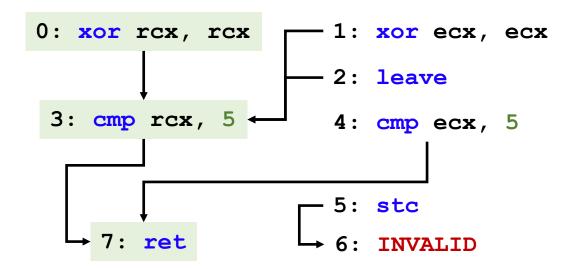
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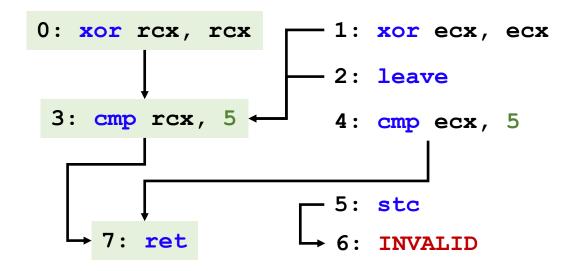




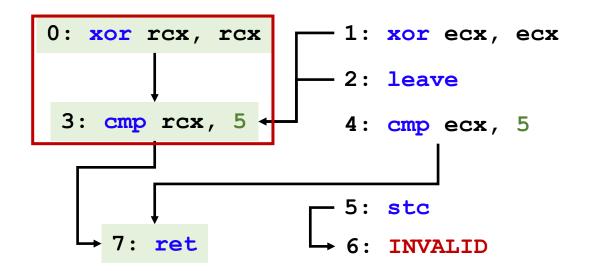




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2	:	с9	1	[1]	leave
3	:	48	1	[4]	cmp rcx, 5
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6	:	05	1	[0]	INVALID
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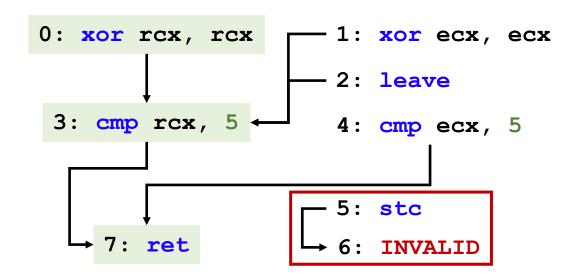
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The control flow cannot reach invalid instructions or data

• Address 5 cannot be a valid instruction boundary as it leads to an *invalid* instruction.

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We also address a number of practical challenges

- Rewriting optimization (e.g., removing flag register saving)
- Supporting stack unwinding (e.g., exception handling in C++)
- Reducing process set up cost
- Safeguarding non-crashing rewriting errors
- Handling overlapping rewriting

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Benchmark:

- Google Fuzzer Test Suite (Google FTS)
- Google Fuzzer Test Suite w/ inlined data
- Fuzzing benchmark from RetroWrite

Baselines:

- <u>E9patch</u>: static binary rewriting [PLDI'20]
- <u>Datalog Disassembly</u>: static binary rewriting [USENIX Security'20]
- *RetroWrite*: static binary rewriting [S&P'20]
- <u>PTFuzzer</u>: hardware-assisted fuzzing [IEEE Access'18]
- <u>AFL-Qemu</u>: dynamic binary translation
- <u>AFL-GCC</u>: compiler-based instrumentation
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Evaluation

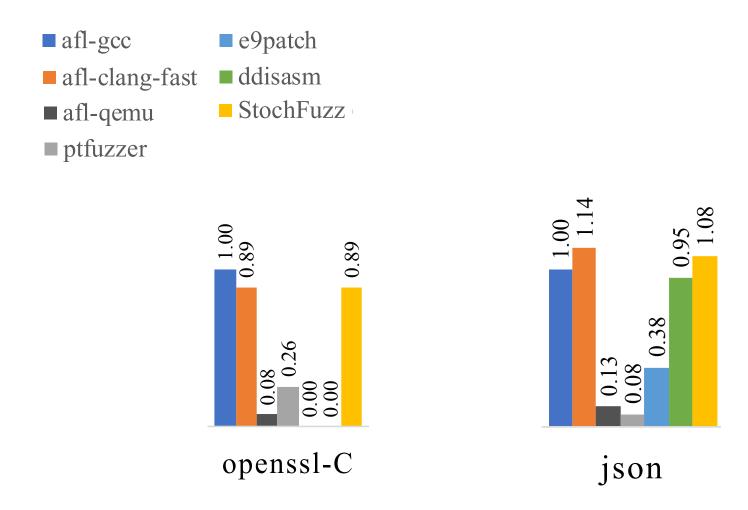
Benchmark:

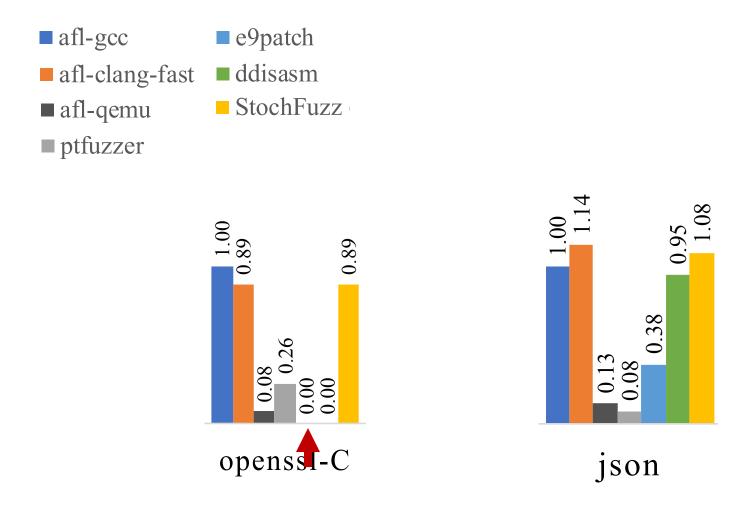
- Google Fuzzer Test Suite (Google FTS)
- Google Fuzzer Test Suite w/ inlined data
- Fuzzing benchmark from RetroWrite

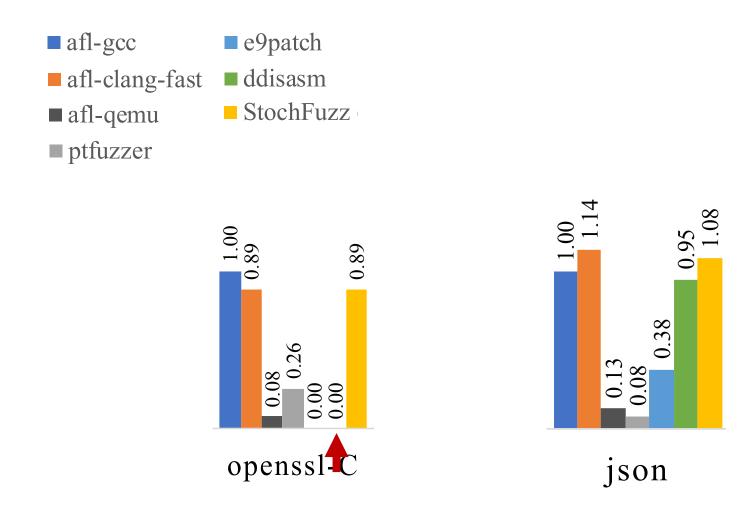
Baselines:

- <u>E9patch</u>: static binary rewriting [PLDI'20]
- <u>Datalog Disassembly</u>: static binary rewriting [USENIX Security'20]
- *RetroWrite*: static binary rewriting [S&P'20]
- <u>PTFuzzer</u>: hardware-assisted fuzzing [IEEE Access'18]
- <u>AFL-Qemu</u>: dynamic binary translation
- <u>AFL-GCC</u>: compiler-based instrumentation
- <u>AFL-Clang-fast</u>: compiler-based instrumentation

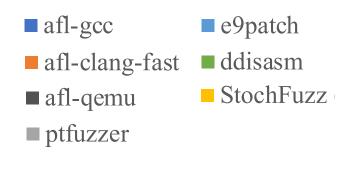
STOCHFUZZ: Sound and Cost-effective Fuzzing of Stripped Binaries by Incremental and Stochastic Rewriting

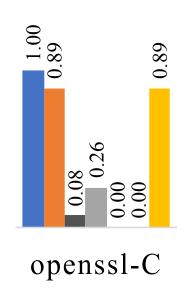


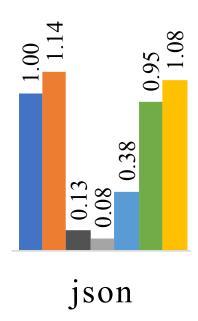




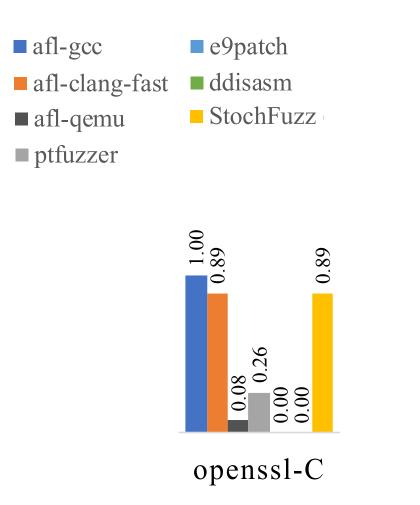
- Existing static rewriting techniques (e9patch and datalog disasm) fail on 12.5–37.5% of the programs, while StochFuzz succeeds on all the 24 programs.
- Compared with *afl-clang-fast*, the IR-based instrumentation, StochFuzz only has **11.77%** slowdown on average.
- Other tools have relatively higher overhead.
 - AFL-Qemu: **88.71%**
 - PTFuzzer: **75.81%**

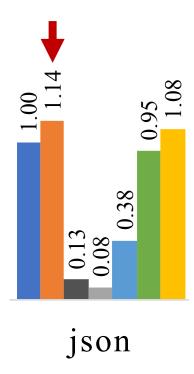




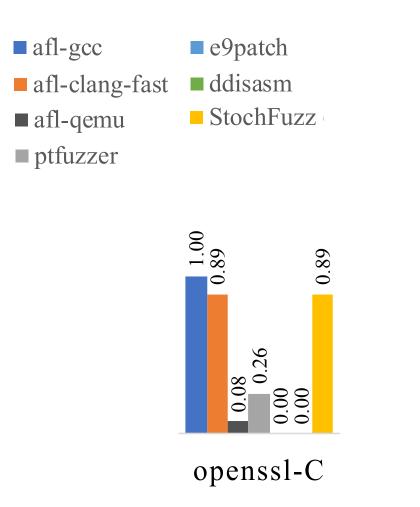


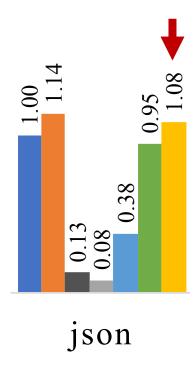
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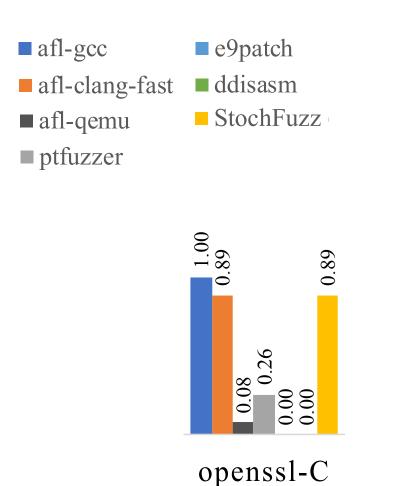


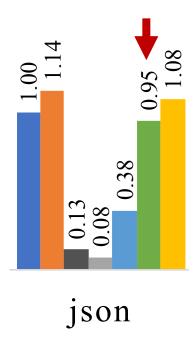
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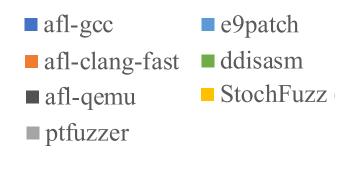


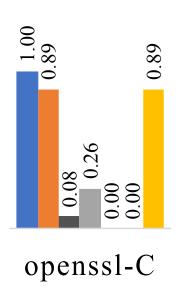
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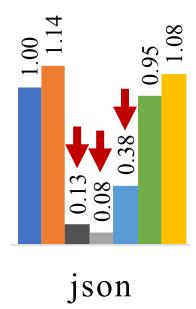




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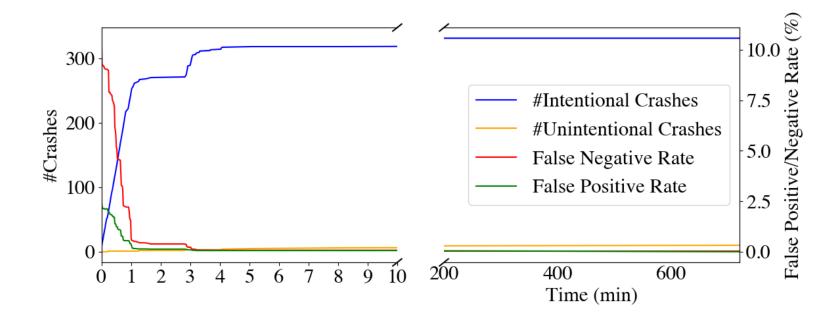
STOCHFUZZ: Sound and Cost-effective Fuzzing of Stripped Binaries by Incremental and Stochastic Rewriting

Evaluation: Progress of Incremental and Stochastic Rewriting (*freetype2*)

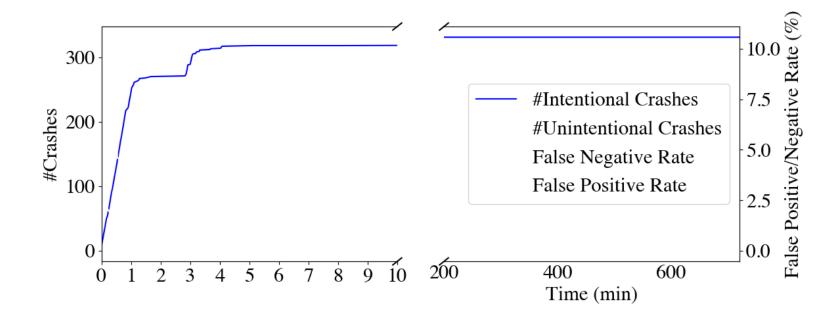
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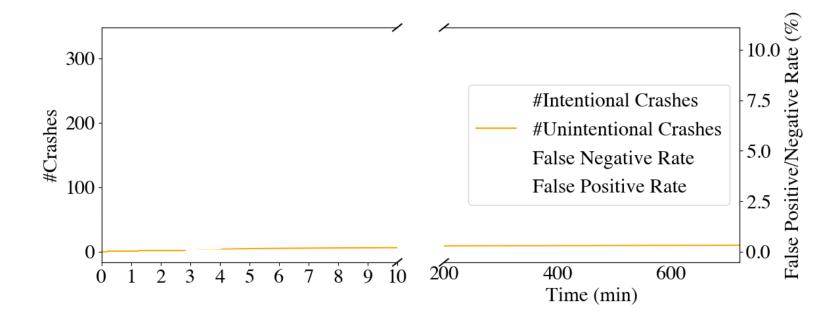
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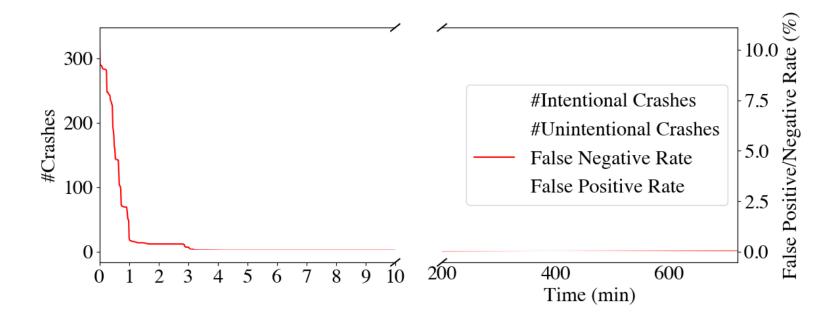
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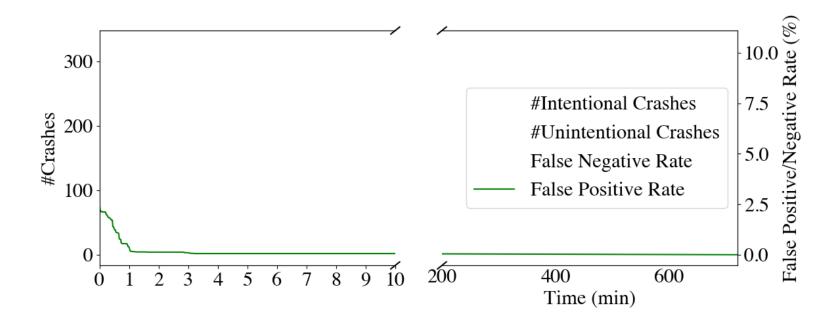
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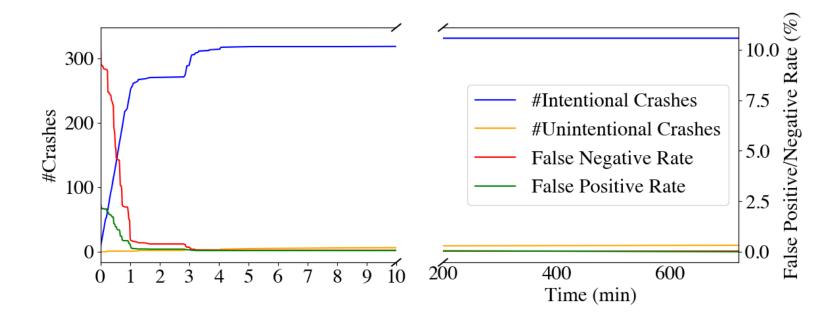
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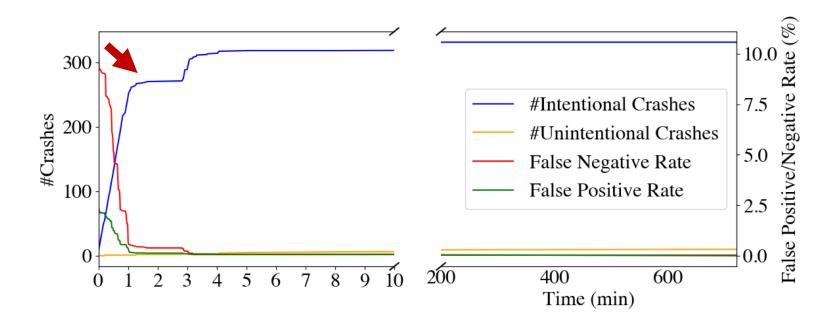


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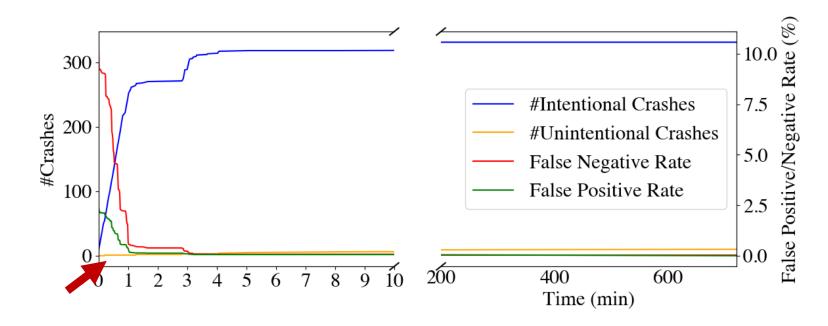
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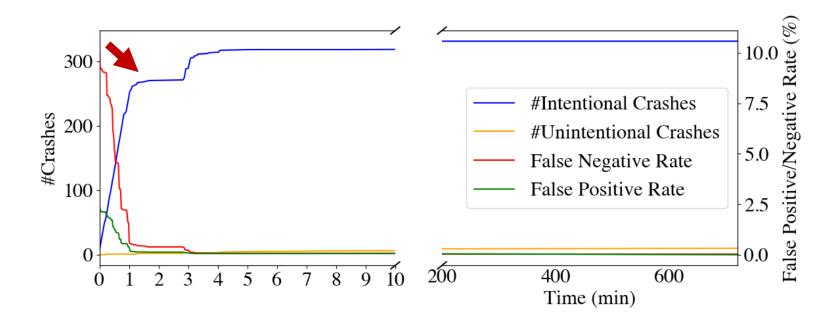
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```
while (...) {
    afl_coverage();
    char c = input();
    if (c == 'A') {
        afl_coverage();
        x = change(x, c);
    } else {
        afl_coverage();
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    }
}
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AFL Instrumentation

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IJON Instrumentation

- *IJON*: state-aware fuzzing [S&P'20]
- We port IJON to support binary-only fuzzing based on AFL-Qemu and STOCHFUZZ
- The same maze experiment
- STOCHFUZZ is 8× faster than afl-qemu, and only has around 8% slowdown compared with source-code based IJON

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IJON Instrumentation

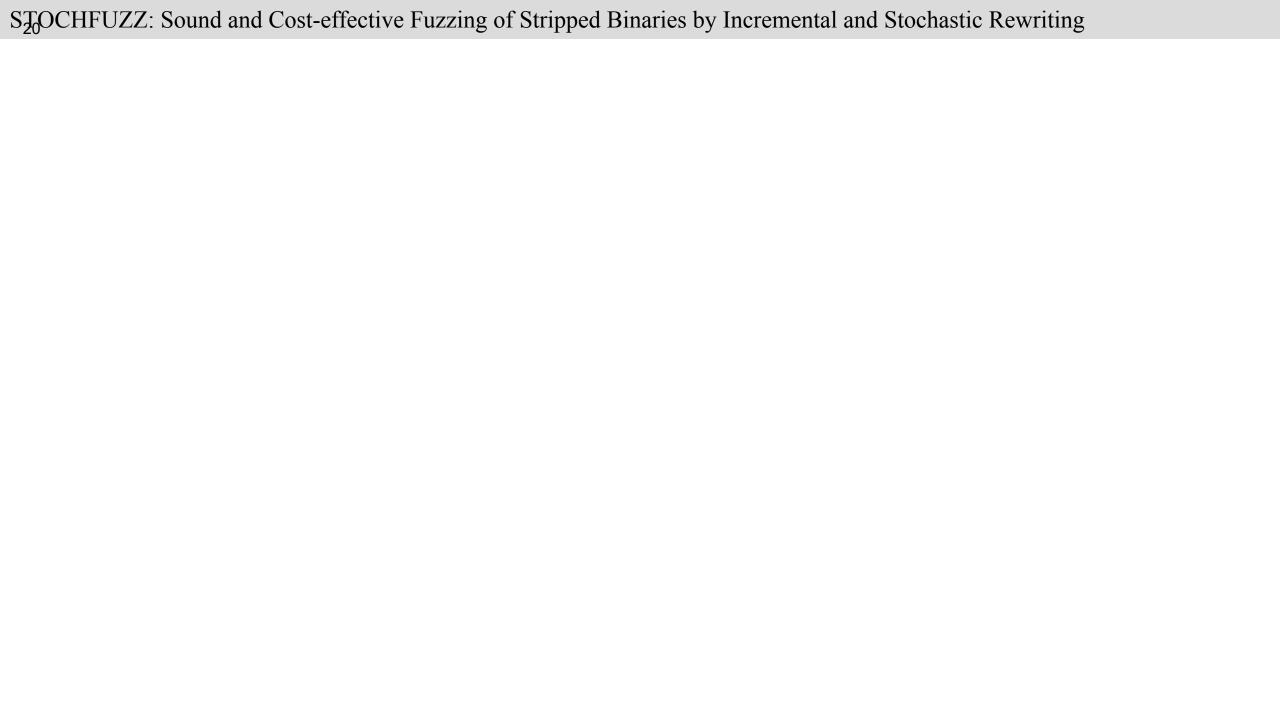
Related Works

Binary Rewriting and Binary-only Fuzzing:

- Flores-Montoya, Antonio, and Eric Schulte. "Datalog disassembly." 29th {USENIX} Security Symposium ({USENIX} Security 20). 2020.
- Duck, Gregory J., Xiang Gao, and Abhik Roychoudhury. "Binary rewriting without control flow recovery." *Proceedings of the 41st ACM SIGPLAN Conference on Programming Language Design and Implementation*. 2020.
- Dinesh, Sushant, et al. "Retrowrite: Statically instrumenting cots binaries for fuzzing and sanitization." 2020 IEEE Symposium on Security and Privacy (SP). IEEE, 2020.
- Zhang, Gen, et al. "Ptfuzz: Guided fuzzing with processor trace feedback." *IEEE Access* 6 (2018): 37302-37313.
- Chen, Yaohui, et al. "Ptrix: Efficient hardware-assisted fuzzing for cots binary." *Proceedings of the 2019 ACM Asia Conference on Computer and Communications Security*. 2019.
- S.Schumilo, C.Aschermann, R.Gawlik, S.Schinzel, and T.Holz, "kafl: Hardware-assisted feedback fuzzing for {OS} kernels," in USENIX Security, 2017, pp. 167–182.

Probabilistic Program Analysis:

- Borges, Mateus, et al. "Iterative distribution-aware sampling for probabilistic symbolic execution." *Proceedings of the 2015 10th Joint Meeting on Foundations of Software Engineering*. 2015.
- Miller, Kenneth, et al. "Probabilistic disassembly." 2019 IEEE/ACM 41st International Conference on Software Engineering (ICSE). IEEE, 2019.



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Thanks!



Github Repo



zhan3299@purdue.edu