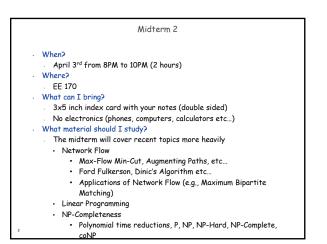
CS 580: Algorithm Design and Analysis

Jeremiah Blocki Purdue University Spring 2019

Homework 5. Due Tonight at 11:59 PM (on Gradescope)

Midterm 2. April 3 © 8PM (EE 170) Practice Midterm Released 3x5 Index Card (Double Sided) Schedule Change: Class canceled on April 4 (We will have class on April 25th)

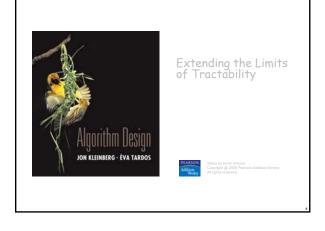


Midterm 2

· When?

- April 3rd from 8PM to 10PM (2 hours)
- Where?
- EE 170
- What material should I study?
 - The midterm will cover recent topics more heavily

 Network Flow
 - · Network Flow
 - Max-Flow Min-Cut, Augmenting Paths, etc...
 - Ford Fulkerson, Dinic's Algorithm etc...
 - Applications of Network Flow (e.g., Maximum Bipartite Matching)
 - Linear Programming
 - NP-Completeness
 - Polynomial time reductions, P, NP, NP-Hard, NP-Complete,
 - coNP
 - PSPACE (only basic questions)



Coping With NP-Completeness

 $\mathbf{Q},~\mathsf{Suppose}\ \mathbf{I}$ need to solve an NP-complete problem. What should \mathbf{I} do?

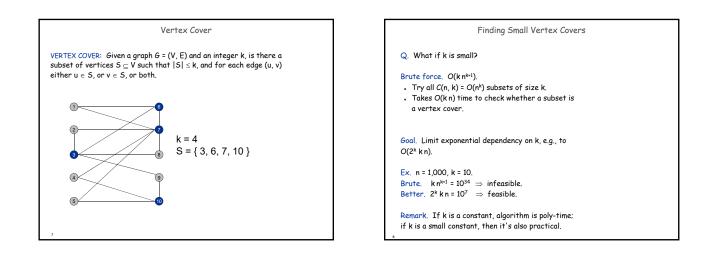
A. Theory says you're unlikely to find poly-time algorithm.

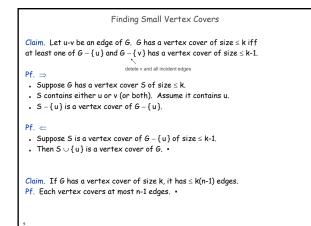
Must sacrifice one of three desired features.

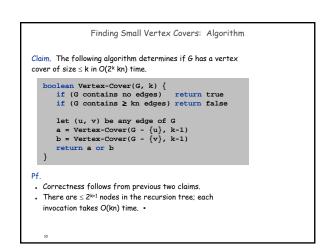
- Solve problem to optimality.
- Solve problem in polynomial time.
- . Solve arbitrary instances of the problem.

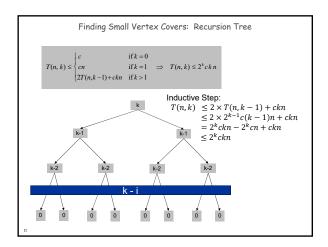
This lecture. Solve some special cases of NP-complete problems that arise in practice.

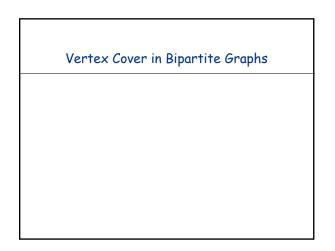
10.1 Finding Small Vertex Covers

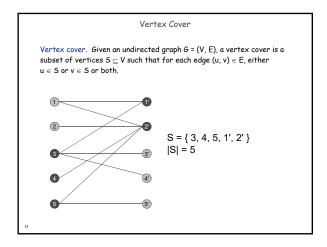


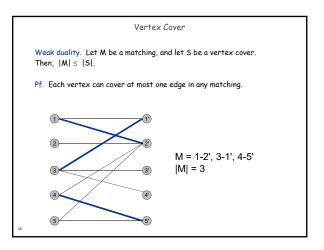


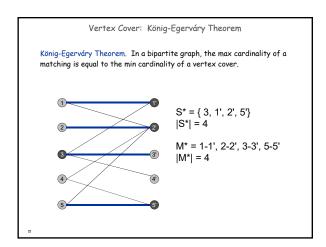


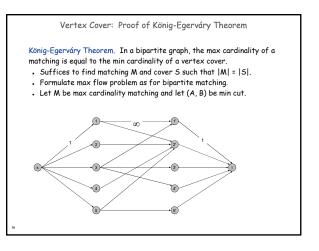


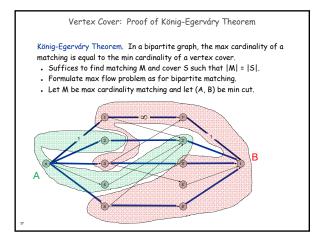


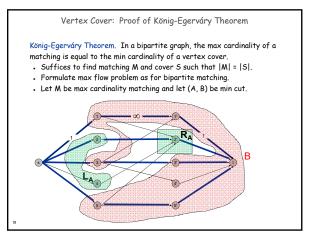


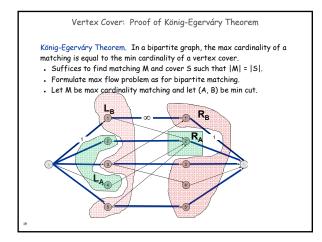


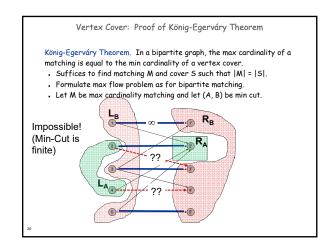


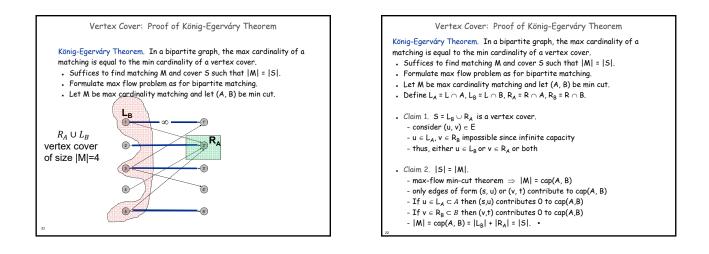


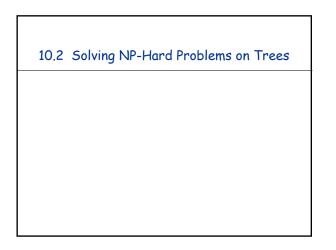


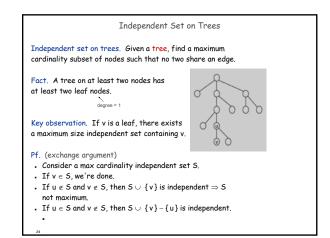


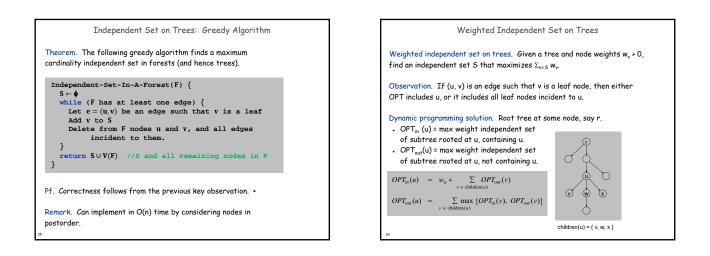


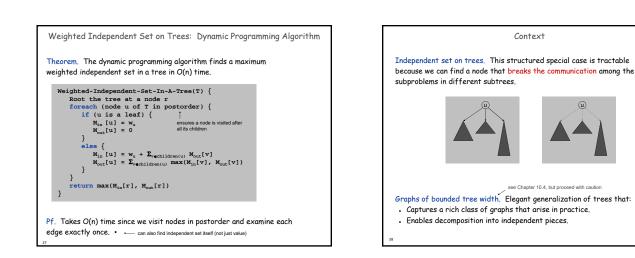












Register Allocation			

Register	Allocation

Register. One of k of high-speed memory locations in computer's CPU. $$$_{say 32}$$

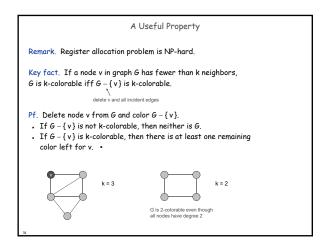
Register allocator. Part of an optimizing compiler that controls which variables are saved in the registers as compiled program executes.

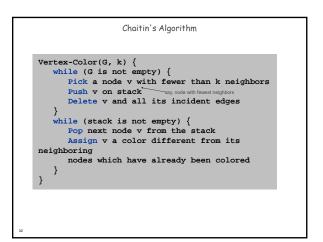
Interference graph. Nodes are "live ranges." Edge u-v if there exists an operation where both u and v are "live" at the same time.

Observation. [Chaitin, 1982] Can solve register allocation problem iff interference graph is k-colorable.

Spilling. If graph is not k-colorable (or we can't find a k-coloring), we "spill" certain variables to main memory and swap back as needed. ``

typically infrequently used variables that are not in inner loops





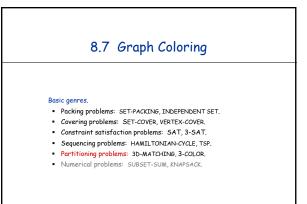
Chaitin's Algorithm

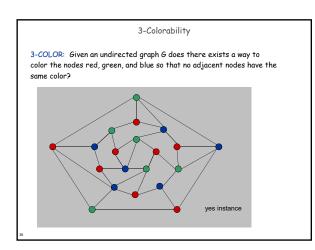
Theorem. [Kempe 1879, Chaitin 1982] Chaitin's algorithm produces a k-coloring of any graph with max degree k-1. Pf. Follows from key fact since each node has fewer than k neighbors.

algorithm succeeds in k-coloring many graphs with max degree $\ge k$

Remark. If algorithm never encounters a graph where all nodes have degree $\geq k$, then it produces a k-coloring.

Practice. Chaitin's algorithm (and variants) are extremely effective and widely used in real compilers for register allocation.





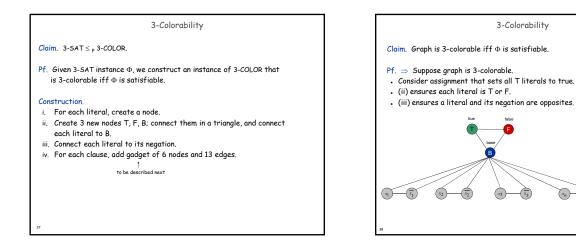
Register Allocation

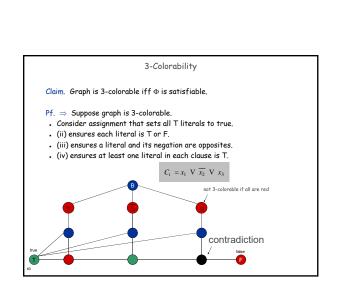
Register allocation. Assign program variables to machine register so that no more than k registers are used and no two program variables that are needed at the same time are assigned to the same register.

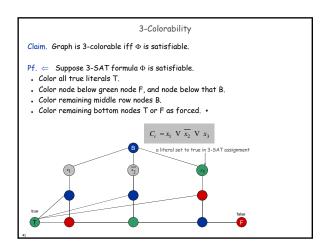
Interference graph. Nodes are program variables names, edge between u and v if there exists an operation where both u and v are "live" at the same time.

Observation. [Chaitin 1982] Can solve register allocation problem iff interference graph is k-colorable.

Fact. 3-COLOR \leq_{P} k-REGISTER-ALLOCATION for any constant k \geq 3.







3-Colorability

 $C_i = x_1 \ V \ \overline{x_2} \ V \ x_3$

Claim. Graph is 3-colorable iff Φ is satisfiable.

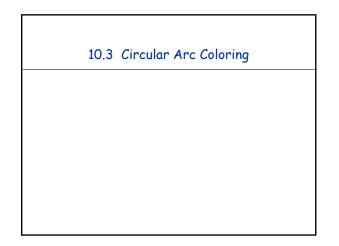
. Consider assignment that sets all T literals to true.

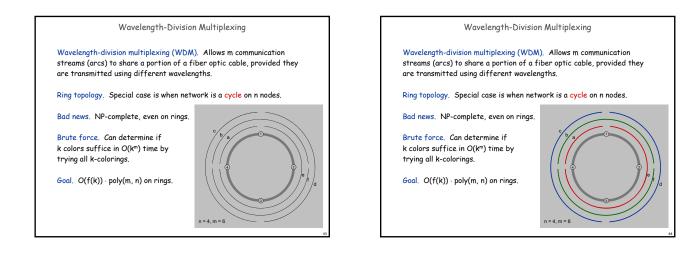
. (iii) ensures a literal and its negation are opposites.

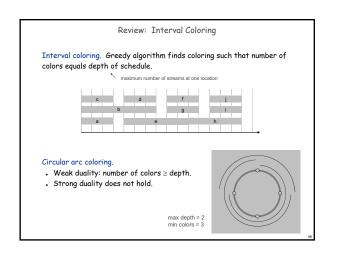
. (iv) ensures at least one literal in each clause is T.

Pf. \Rightarrow Suppose graph is 3-colorable.

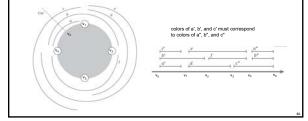
• (ii) ensures each literal is T or F.

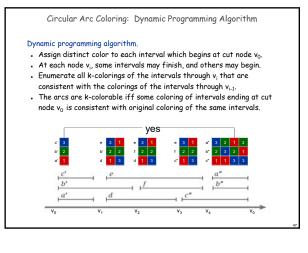






(Almost) Transforming Circular Arc Coloring to Interval Coloring Circular arc coloring. Given a set of n arcs with depth $d \le k$, can the arcs be colored with k colors? Equivalent problem. Cut the network between nodes v_1 and v_n . The arcs can be colored with k colors iff the intervals can be colored with k colors in such a way that "sliced" arcs have the same color.



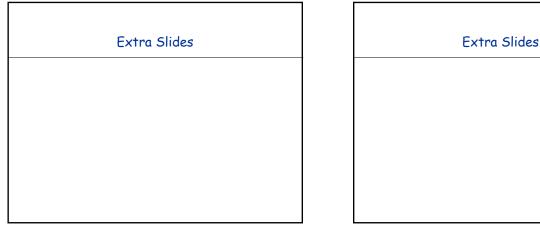


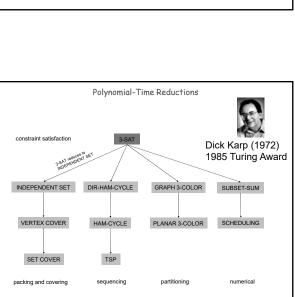


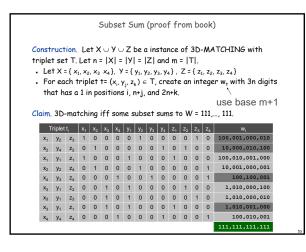
- Running time. O(k! · n).
- n phases of the algorithm.
- Bottleneck in each phase is enumerating all consistent colorings. There are at most k intervals through $v_{i,}$ so there are at most k!
- colorings to consider.

Remark. This algorithm is practical for small values of k (say k = 10)

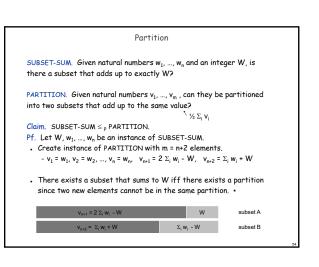
even if the number of nodes n (or paths) is large.

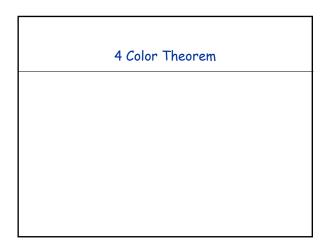


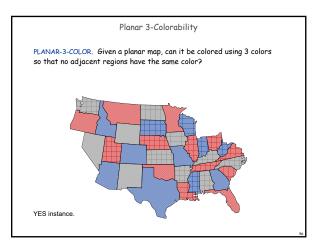


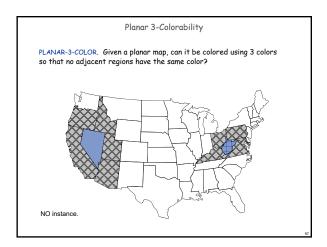


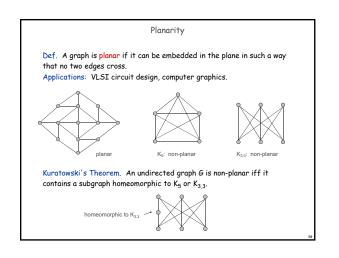
8.10 A Partial Taxonomy of Hard Problems









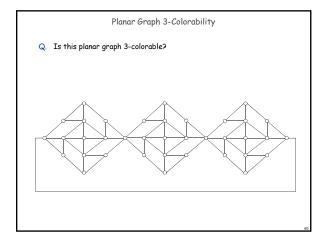


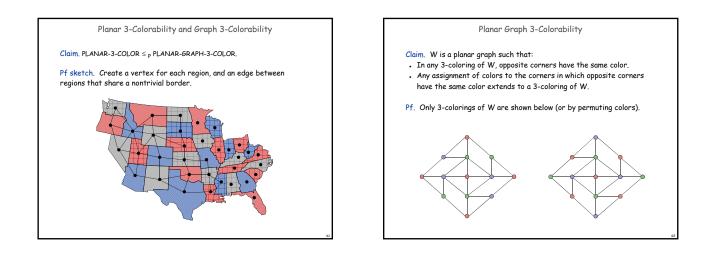
Planarity Testing

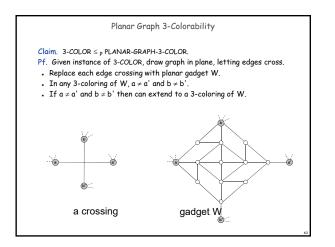
Planarity testing. [Hopcroft-Tarjan 1974] O(n).

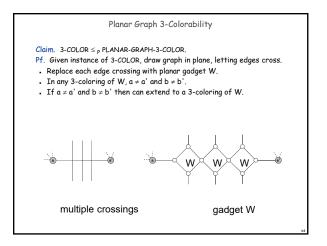
simple planar graph can have at most 3n edges

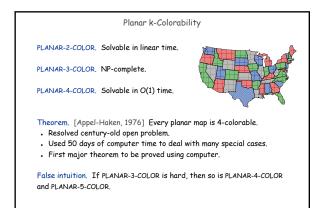
Remark. Many intractable graph problems can be solved in poly-time if the graph is planar; many tractable graph problems can be solved faster if the graph is planar.

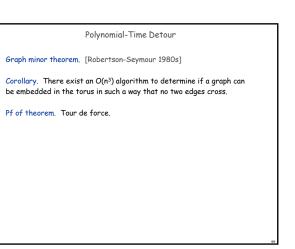












Polynomial-Time Detour

Graph minor theorem. [Robertson-Seymour 1980s]

Corollary. There exist an $O(n^3)$ algorithm to determine if a graph can be embedded in the torus in such a way that no two edges cross.

Mind boggling fact 1. The proof is highly non-constructive! Mind boggling fact 2. The constant of proportionality is enormous!

> Unfortunately, for any instance G = (V, E) that one could fit into the known universe, one would easily prefer n^{20} to even *constant* time, if that constant had to be one of Robertson and Seymour's. - David Johnson

Theorem. There exists an explicit O(n) algorithm. Practice. LEDA implementation guarantees $O(n^3)$.